3300V Silicon N-channel IGBT F version with SiC Diode

FEATURES

* Soft switching & low conduction loss IGBT:

Soft low-injection punch-through

High conductivity IGBT with advanced trench MOS gate.

- * Low driving power due to low input capacitance.
- * Ultra low recovery loss with SiC diode.
- * High Current rate Package.
- * Low stray inductance.
- * RoHS

ABSOLUTE MAXIMUM RATINGS (T_C=25°C)

Item		Symbol	Unit	MBN1800F33F-C3
Collector Emitter Voltage		V _{CES}	V	3,300
Gate Emitter Voltage		V _{GES}	V	±20
Collector Current	DC	Ic	^	1,800
	1ms	I _{CRM}	A	3,600
Forward Current	DC	I _F	Λ	1,800
	1ms	I _{FRM}	— A	3,600
Junction Temperature		T _{vj op}	°C	-40 ~ + 150
Storage Temperature		T _{stg}	°C	-40 ~ +150
Isolation Voltage		V _{ISO}	V _{RMS}	6,000(AC 1 minute)
Screw Torque	Terminals (M4/M8)	-	N·m	2/15 (1)
	Mounting (M6)	-	IN-M	6 (2)

Notes: (1) Recommended Value 1.8±0.2/15⁺⁰-3N·m (2) Recommended Value 5.5±0.5N·m

ELECTRICAL CHARACTERISTICS

- Item	Symbol	Unit	Min.	Typ.	Max.	Test Conditions
Collector Emitter Cut-Off Current	I _{CES}	mA	-	-	18	V _{CE} =3,300V, V _{GE} =0V, T _{vi} =25°C
Collector Emitter Cut-On Current			-	38	-	V _{CE} =3,300V, V _{GE} =0V, T _{vj} =150°C
Gate Emitter Leakage Current	I _{GES}	nΑ	-500	-	+500	$V_{GE}=\pm 20V, V_{CE}=0V, T_{vj}=25^{\circ}C$
Collector Emitter Saturation Voltage	V _{CEsat}	V	-	2.85	-	I _C =1,800A, V _{GE} =15V, T _{Vj} =150°C
Gate Emitter Threshold Voltage	$V_{GE(th)}$	V	5.5	6.5	7.5	V _{CE} =10V, I _C =1,800mA, T _{vi} =25°C
Input Capacitance	Cies	nF	-	132	-	$V_{CE}=10V$, $V_{GE}=0V$, $f=100kHz$, $T_{vj}=25^{\circ}C$
Internal Gate Resistance	R _{G(int)}	Ω	-	1.3	-	$V_{CE}=10V, V_{GE}=0V, f=100kHz, T_{vj}=25^{\circ}C$
Turn On Delay Time	t _{d(on)}		-	0.8	-	V_{CC} =1,800V, I_{C} =1,800A
Rise Time	t _r	μS	-	0.3	-	L _S =80nH
Turn Off Delay Time	t _{d(off)}	μδ	-	2.5	-	$R_G(\text{on/off})=4.7\Omega/5.6\Omega$ (3)
Fall Time	t _f		-	1.7	-	$V_{GE}=\pm 15V, T_{vj}=150^{\circ}C$
Peak Forward Voltage Drop	V _F	V	-	4.6	-	$I_F=1,800A, V_{GE}=0V, T_{vj}=150^{\circ}C$
Reverse Recovery Time	t _{rr}	μS	-	0.1	-	V _{CC} =1,800V, I _F =1,800A, L _S =80nH T _{vi} =150°C
Turn On Loss	Eon	J/P	-	2.1	-	V _{CC} =1,800V, I _C =1,800A, L _S =80nH
Turn Off Loss	E _{off}	J/P	-	3.3	-	$R_G(\text{on/off})=4.7\Omega/5.6\Omega$ (3)
Reverse Recovery Loss	Err	J/P	-	(0.15)	-	$V_{GE}=\pm 15V$, $T_{vi}=150^{\circ}C$
Short Circuit Pulse Width	t _{sc}	μS	10	ı		V _{CC} =2,200V,Ls=80nH
Short Circuit Fuise Width	LSC				-	$R_G(on/off) = 4.7/56\Omega, V_{GE} = \pm 15V, T_{vj} = 150^{\circ}C$
Stray inductance module	L _{SCE}	nΗ	-	7	-	
Thermal Impedance IGBT	R _{th(j-c)}	K/W	-	-	0.0067	Junction to case
	R _{th(j-c)}		-	-	- 0.012 Junction to case	Junioni to case
Contact Thermal Impedance	R _{th(c-f)}	K/W	-	0.005	-	Case to fin

Notes: (3) R_G value is a test condition value for evaluation, not recommended value. Please, determine the suitable R_G value by measuring switching behaviors.

^{*} ELECTRICAL CHARACTERISTIC items shown in above table are according to IEC 60747-2 and IEC 60747-9.

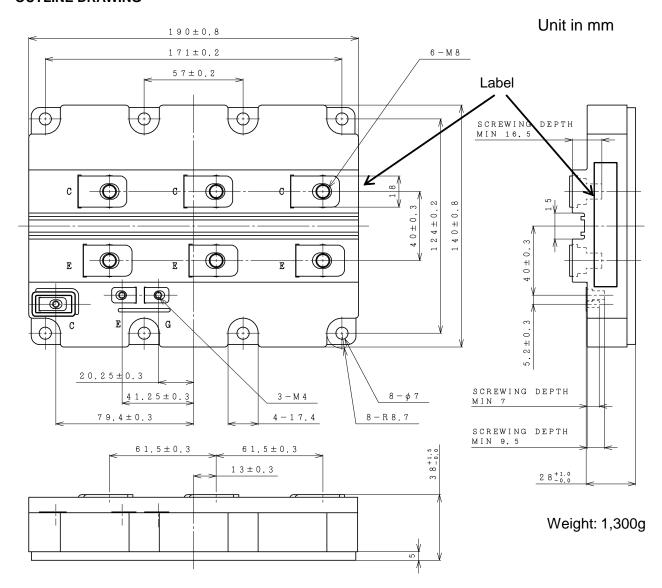


^{*} Please contact our representatives at order.

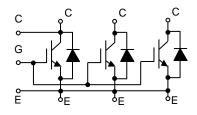
^{*} For improvement, specifications are subject to change without notice.

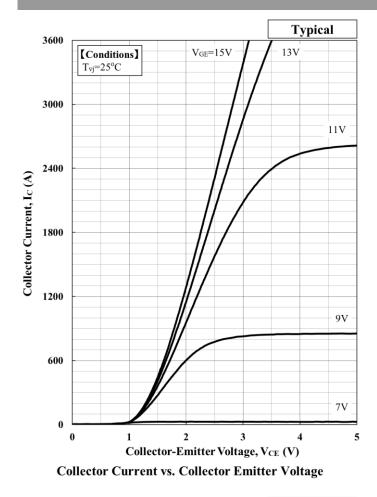
^{*} For actual application, please confirm this spec sheet is the newest revision.

OUTLINE DRAWING

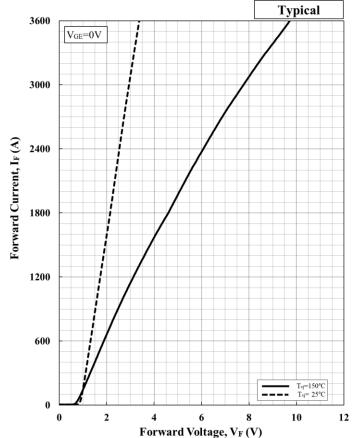


CIRCUIT DIAGRAM

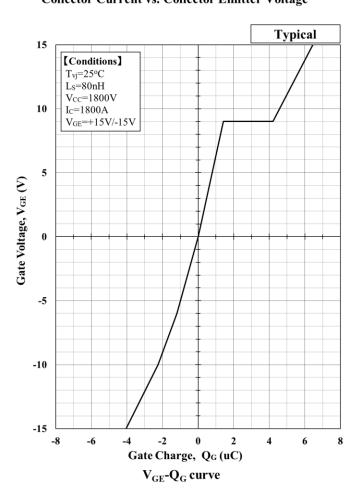




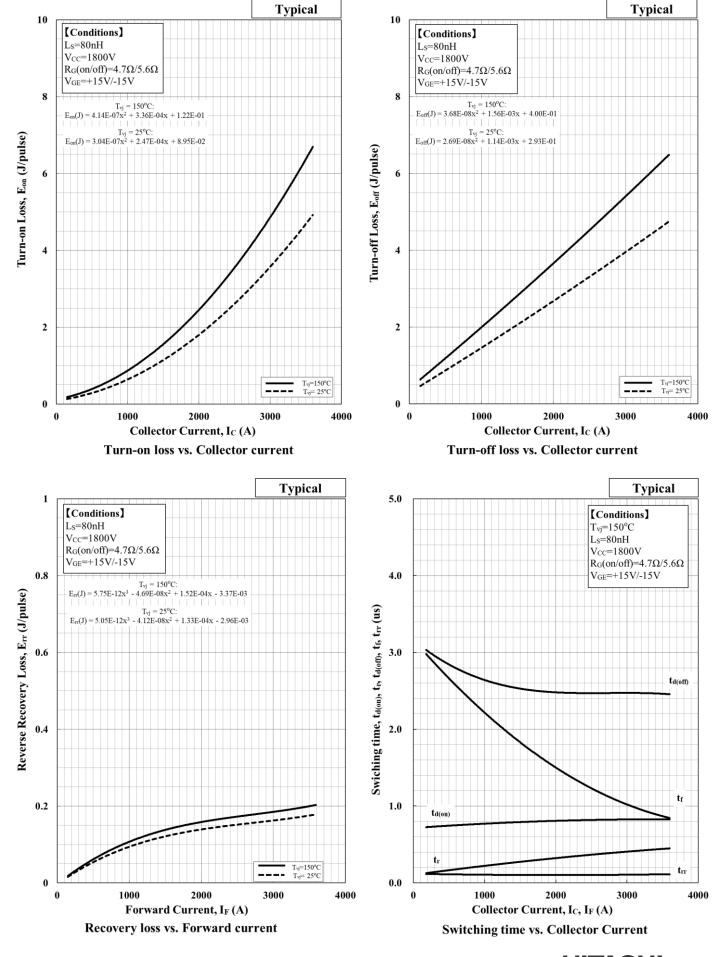
Typical 3600 [Conditions] V_{GE}=15V $T_{vj}=150^{\circ}C$ 3000 2400 Collector Current, Ic (A) 11V 1800 1200 9V 600 7V Collector-Emitter Voltage, VCE (V) Collector Current vs. Collector Emitter Voltage



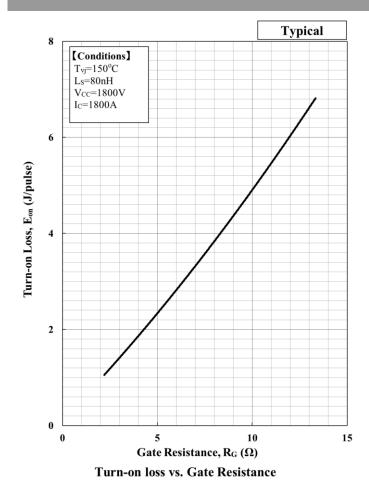
Forward Voltage of free-wheeling diode

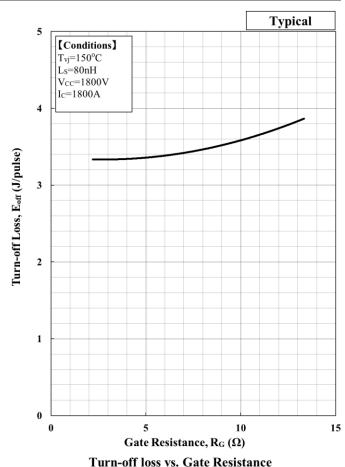


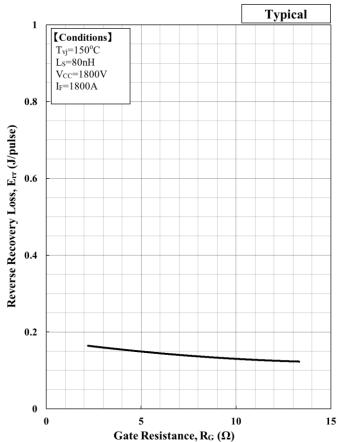




HITACHI Inspire the Next

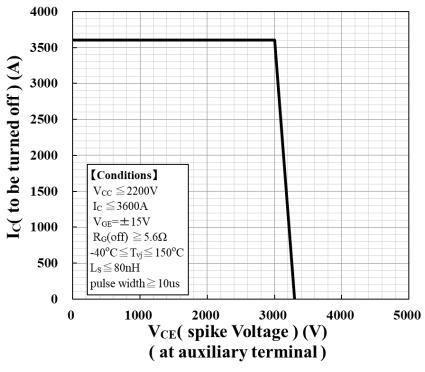


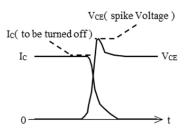




Recovery loss vs. Gate Resistance

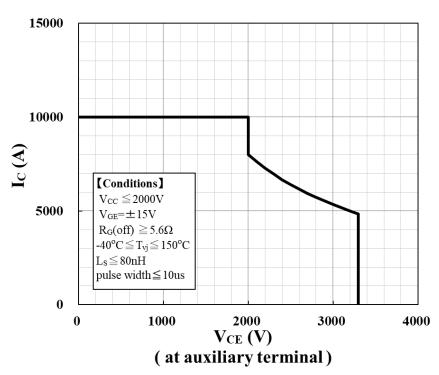






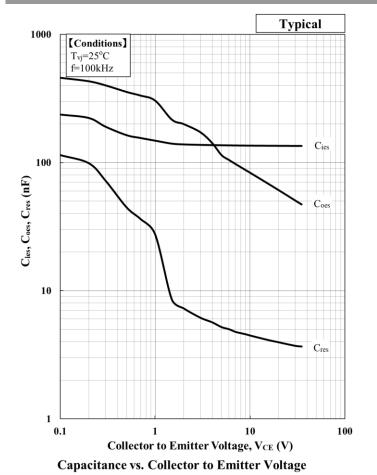
Definition of RBSOA waveform

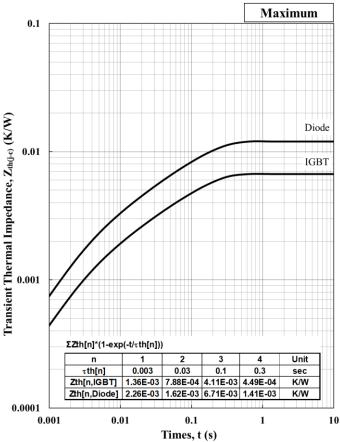
Reverse Bias Safe Operation Area (RBSOA)



Short cuicuit Safe Operation Area (SCSOA)







Transient Thermal Ipedance Curve

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HITACHI POWER SEMICONDUCTORS

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