

MBN1000FH45F

Silicon N-channel IGBT 4500V F version

FEATURES

- * Soft switching behavior, low switching loss & low conduction loss :
Soft low-injection punch-through
Advanced Trench High conductivity IGBT.
- * Low driving power due to low input capacitance with trench MOS gate.
- * Low noise recovery: Ultra soft fast recovery diode.
- * High Current rate Package.
- * Low $R_{th(j-c)}$ & low stray inductance.
- * RoHS

ABSOLUTE MAXIMUM RATINGS ($T_C=25^\circ\text{C}$)

Item	Symbol	Unit	MBN1000FH45F
Collector Emitter Voltage	V_{CES}	V	4,500
Gate Emitter Voltage	V_{GES}	V	± 20
Collector Current	DC	I_C	1,000
	1ms	I_{CRM}	2,000
Forward Current	DC	I_F	1,000
	1ms	I_{FRM}	2,000
Junction Temperature	$T_{vj\text{ op}}$	$^\circ\text{C}$	-50 ~ +150
Storage Temperature	T_{stg}	$^\circ\text{C}$	-50 ~ +150
Isolation Voltage	V_{ISO}	V_{RMS}	10,200(AC 1 minute)
Screw Torque	Terminals (M4/M8)	-	2/10 (1)
	Mounting (M6)	-	6 (2)

Notes: (1) Recommended Value $1.8\pm 0.2/9\pm 1\text{N}\cdot\text{m}$ (2) Recommended Value $5.5\pm 0.5\text{N}\cdot\text{m}$

ELECTRICAL CHARACTERISTICS

Item	Symbol	Unit	Min.	Typ.	Max.	Test Conditions
Collector Emitter Cut-Off Current	I_{CES}	mA	-	-	4	$V_{CE}=4,500\text{V}, V_{GE}=0\text{V}, T_{vj}=25^\circ\text{C}$
			-	40	120	$V_{CE}=4,500\text{V}, V_{GE}=0\text{V}, T_{vj}=150^\circ\text{C}$
Gate Emitter Leakage Current	I_{GES}	nA	-500	-	+500	$V_{GE}=\pm 20\text{V}, V_{CE}=0\text{V}, T_{vj}=25^\circ\text{C}$
Collector Emitter Saturation Voltage	$V_{CE(sat)}$	V	-	3.0	3.4	$I_C=1000\text{A}, V_{GE}=15\text{V}, T_{vj}=150^\circ\text{C}$
Gate Emitter Threshold Voltage	$V_{GE(th)}$	V	6.0	6.5	7.0	$V_{CE}=10\text{V}, I_C=1000\text{mA}, T_{vj}=25^\circ\text{C}$
Input Capacitance	C_{ies}	nF	-	55	-	$V_{CE}=10\text{V}, V_{GE}=0\text{V}, f=100\text{kHz}, T_{vj}=25^\circ\text{C}$
Internal Gate Resistance	$R_{G(int)}$	Ω	-	3.9	-	$V_{CE}=10\text{V}, V_{GE}=0\text{V}, f=100\text{kHz}, T_{vj}=25^\circ\text{C}$
Turn On Delay Time	$t_{d(on)}$	μs	-	0.5	-	$V_{CC}=2,800\text{V}, I_C=1000\text{A}$
Rise Time	t_r		-	0.25	-	$L_S=180\text{nH}$
Turn Off Delay Time	$t_{d(off)}$		-	2.8	-	$R_G(\text{on/off})=4.7/4.7\Omega$ (3)
Fall Time	t_f		-	2.1	-	$V_{GE}=\pm 15\text{V}, T_{vj}=150^\circ\text{C}$
Peak Forward Voltage Drop	V_F	V	-	2.8	3.2	$I_F=1000\text{A}, V_{GE}=0\text{V}, T_{vj}=150^\circ\text{C}$
Reverse Recovery Time	t_{rr}	μs	-	1.3	-	$V_{CC}=2,800\text{V}, I_F=1000\text{A}, L_S=180\text{nH}$ $T_{vj}=150^\circ\text{C}$
Turn On Loss	E_{on}	J/P	-	3.6	-	$V_{CC}=2,800\text{V}, I_C=1000\text{A}, L_S=180\text{nH}$
Turn Off Loss	E_{off}	J/P	-	5.3	-	$R_G(\text{on/off})=4.7/4.7\Omega$ (3)
Reverse Recovery Loss	E_{rr}	J/P	-	3.6	-	$V_{GE}=\pm 15\text{V}, T_{vj}=150^\circ\text{C}$
Short Circuit Pulse Width	t_{sc}	μs	10	-	-	$V_{CC}=3000\text{V}, L_S=180\text{nH}$ $R_G(\text{on/off})=4.7/4.7\Omega, V_{GE}=\pm 15\text{V}, T_{vj}=150^\circ\text{C}$
Partial discharge extinction voltage	V_e	V_{RMS}	3,500	-	-	$f=50\text{Hz}, Q_{PD}\leq 10\text{pC}(\text{acc. to IEC 61287})$
Stray inductance module	L_{SCE}	nH	-	15	-	Collector Main to Emitter Main
Thermal Impedance	IGBT	$R_{th(j-c)}$	-	-	0.013	Junction to case
	FWD	$R_{th(j-c)}$	-	-	0.017	
Contact Thermal Impedance	$R_{th(c-f)}$	K/W	-	0.007	-	Case to fin

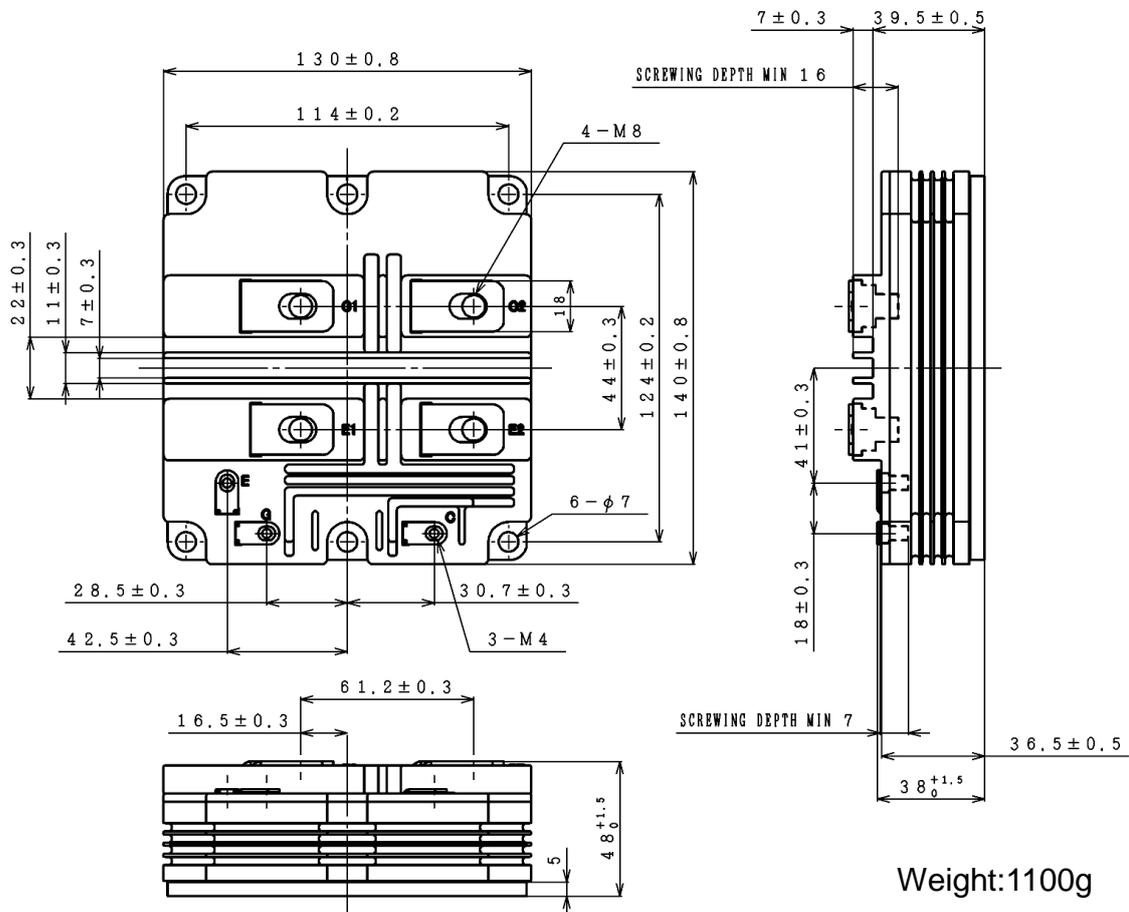
Notes: (3) R_G value is a test condition value for evaluation, not recommended value.
Please, determine the suitable R_G value by measuring switching behaviors.

- * Please contact our representatives at order.
- * For improvement, specifications are subject to change without notice.
- * For actual application, please confirm this spec sheet is the newest revision.
- * ELECTRICAL CHARACTERISTIC items shown in above table are according to IEC 60747-2 and IEC 60747-9.

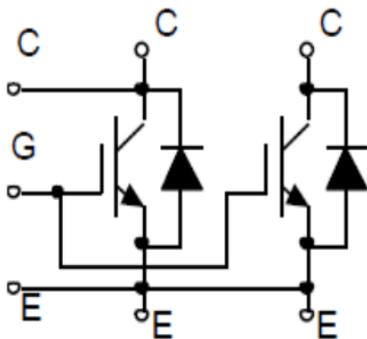
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OUTLINE DRAWING

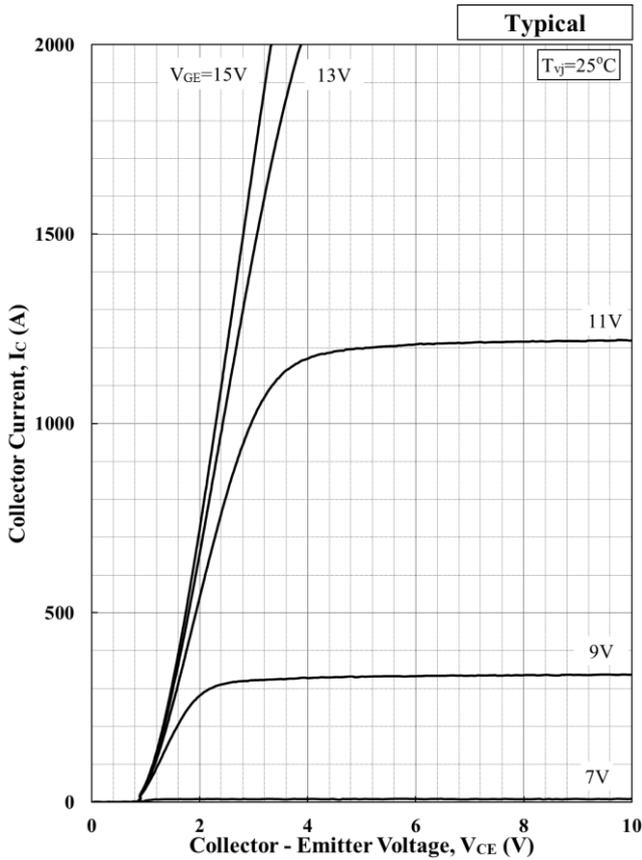
Unit in mm



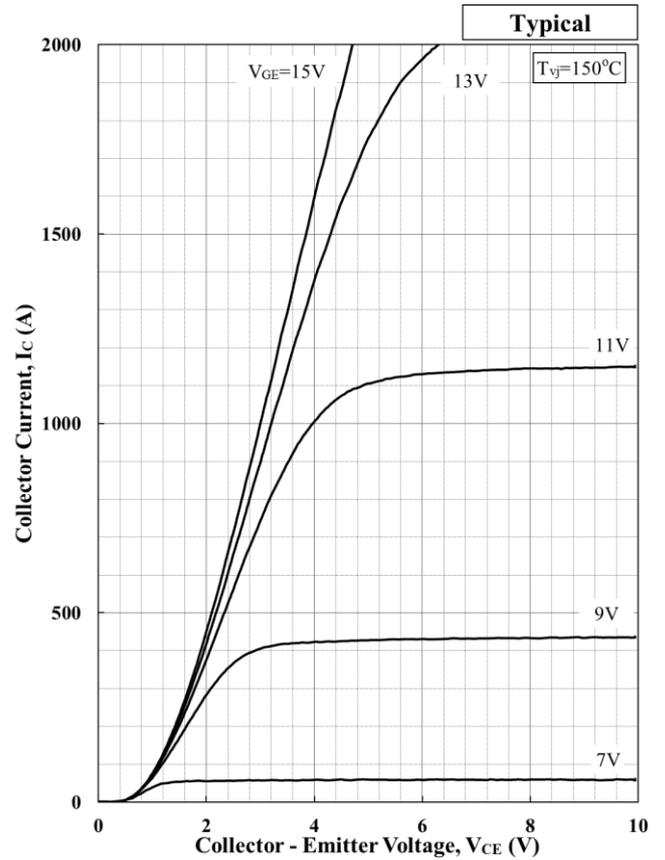
CIRCUIT DIAGRAM



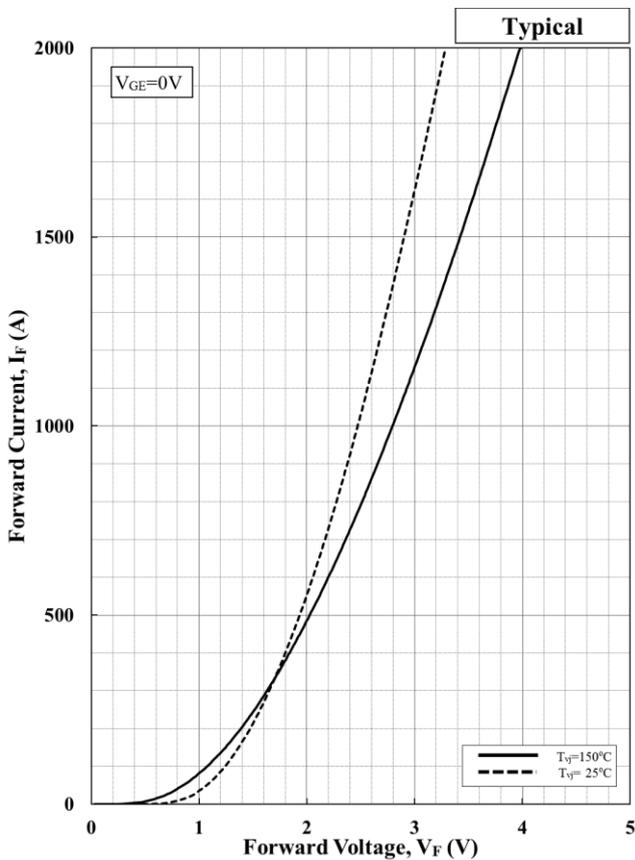
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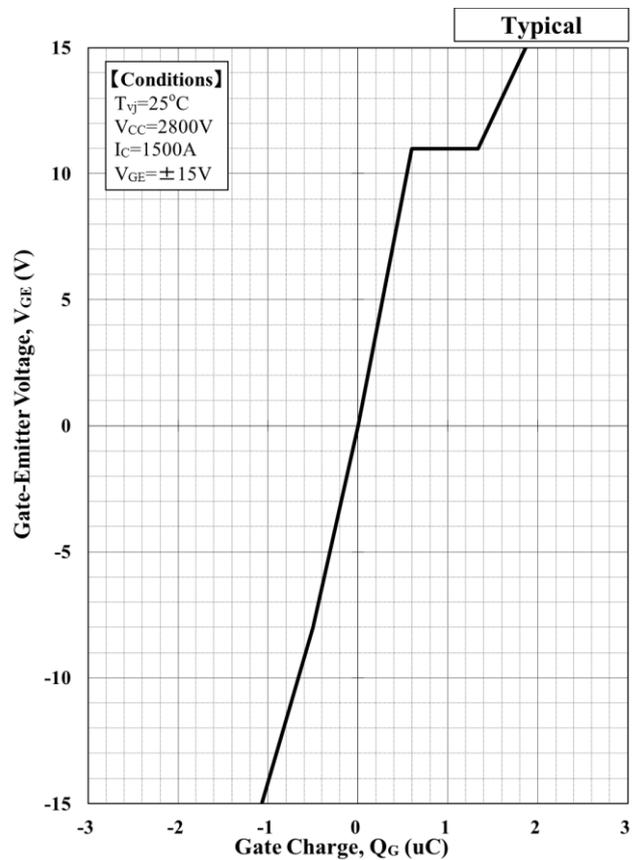
Collector Current vs. Collector to Emitter Voltage



Collector Current vs. Collector to Emitter Voltage

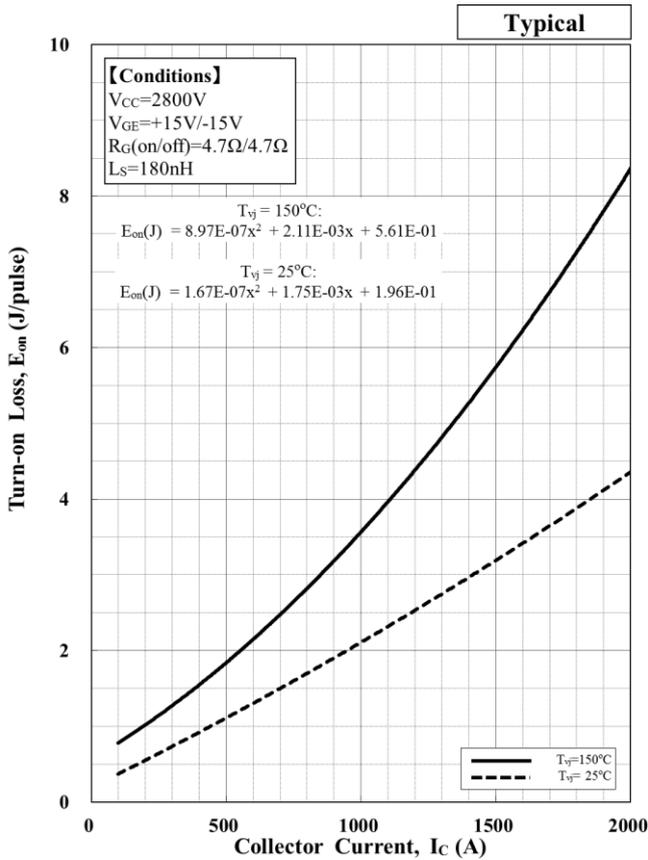


Forward Voltage of free-wheeling diode

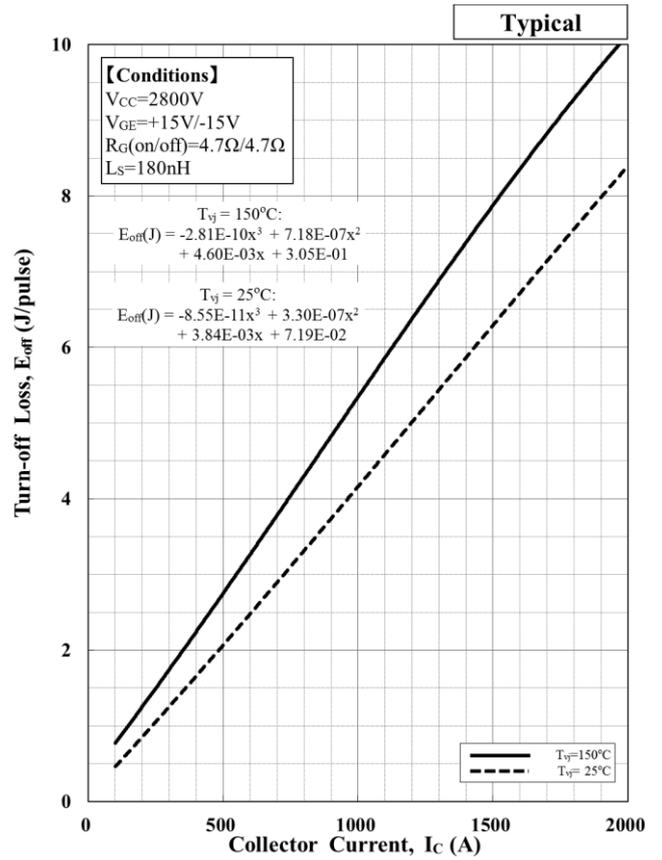


V_{GE} - Q_G curve

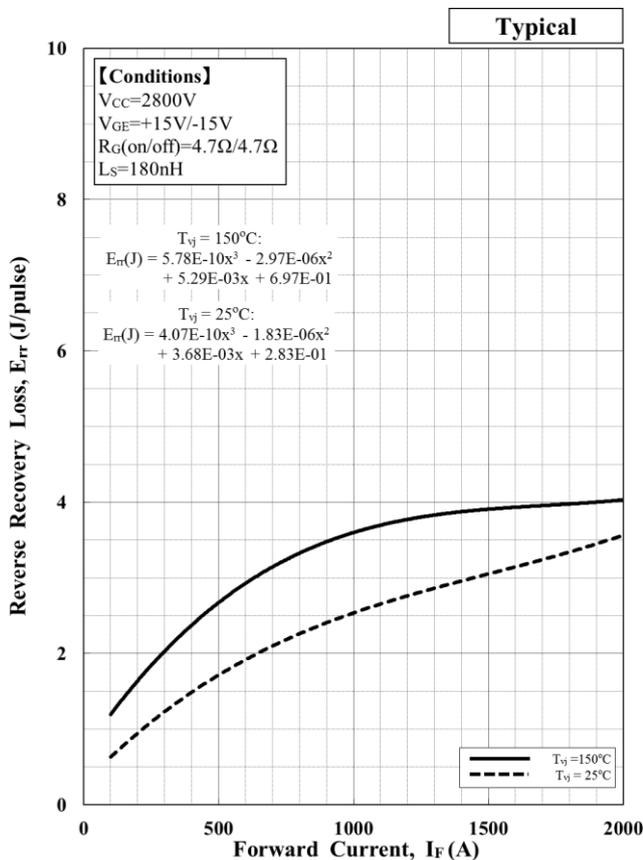
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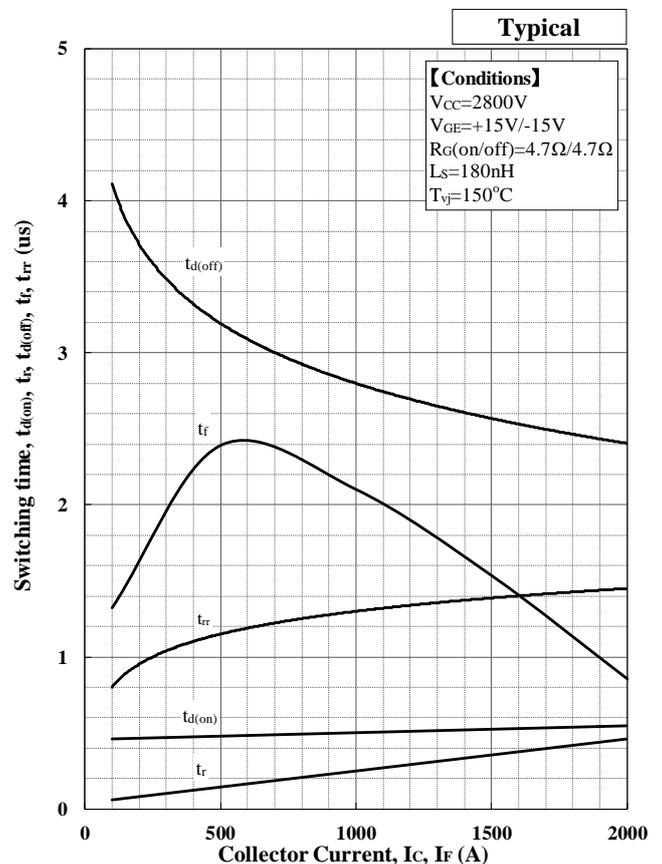
Turn-on loss vs. Collector current



Turn-off loss vs. Collector current

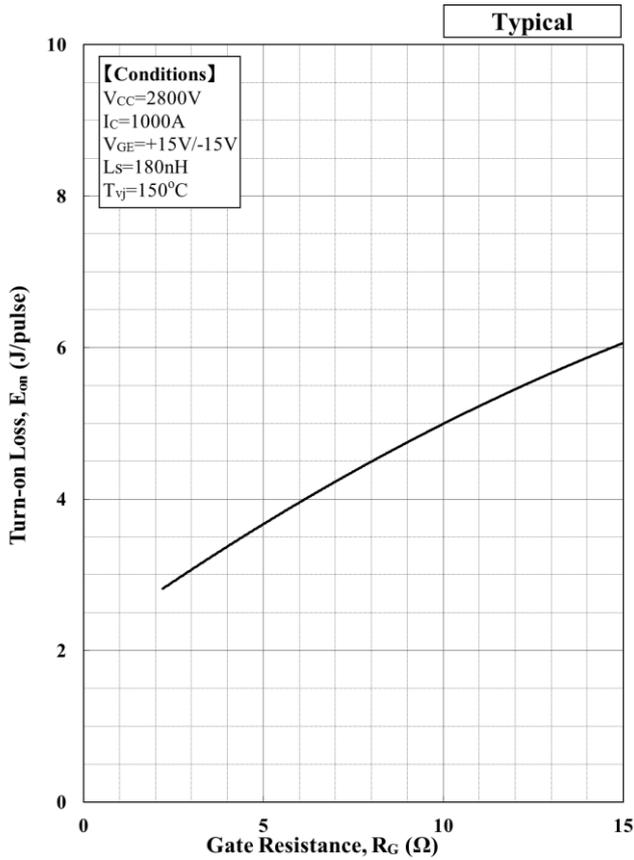


Reverse Recovery loss vs. Forward current

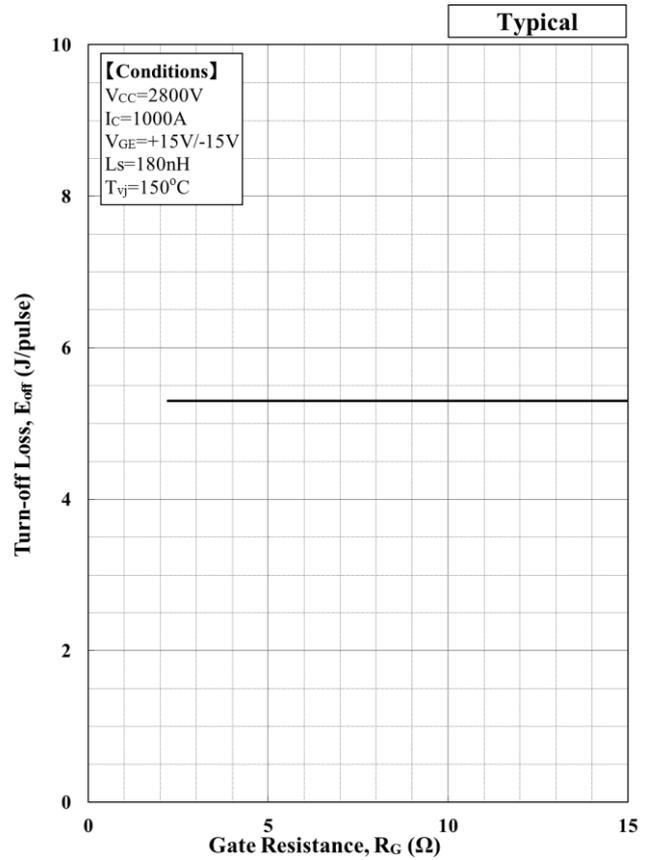


Switching time vs. Collector current

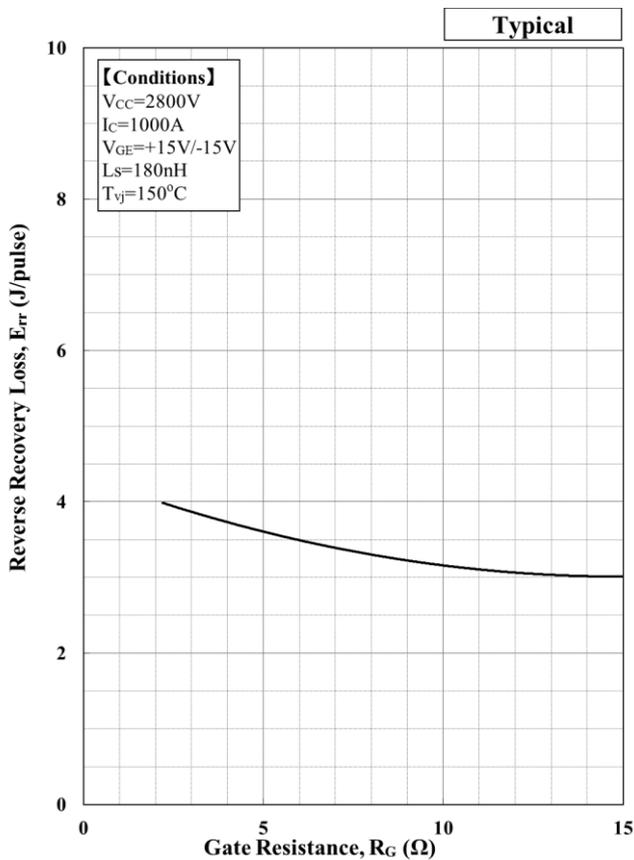
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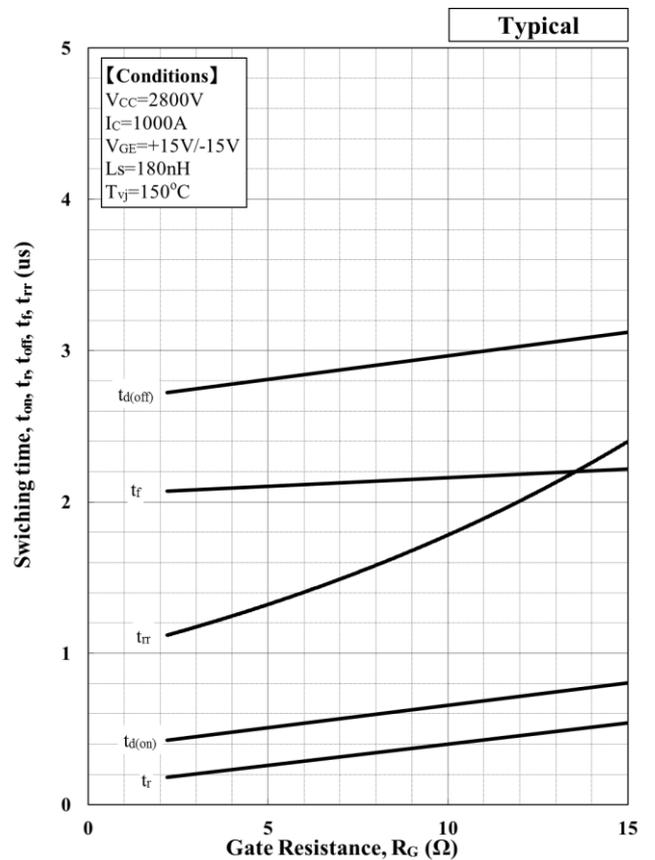
Turn-on loss vs. Gate Resistance



Turn-off loss vs. Gate Resistance

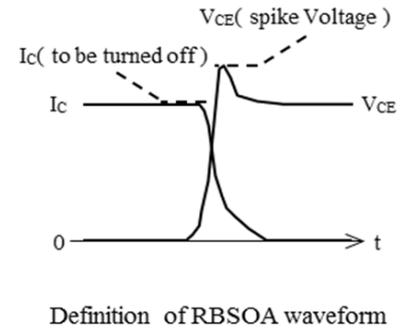
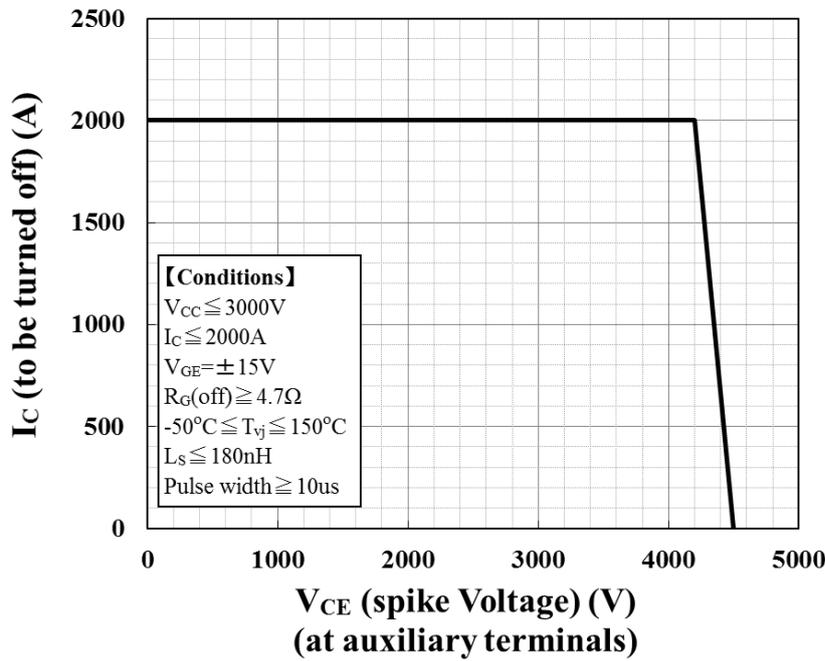


Reverse Recovery loss vs. Gate Resistance

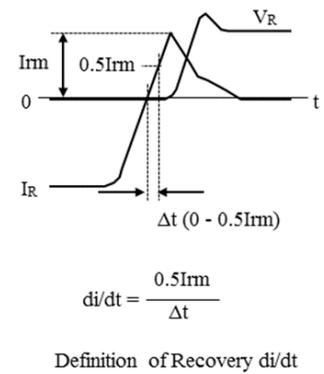
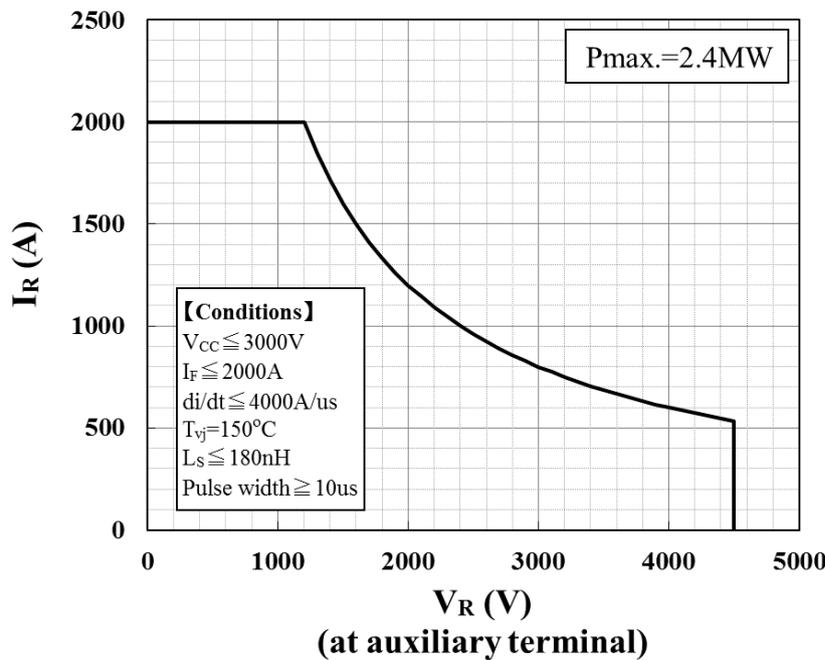


Switching time vs. Gate Resistance

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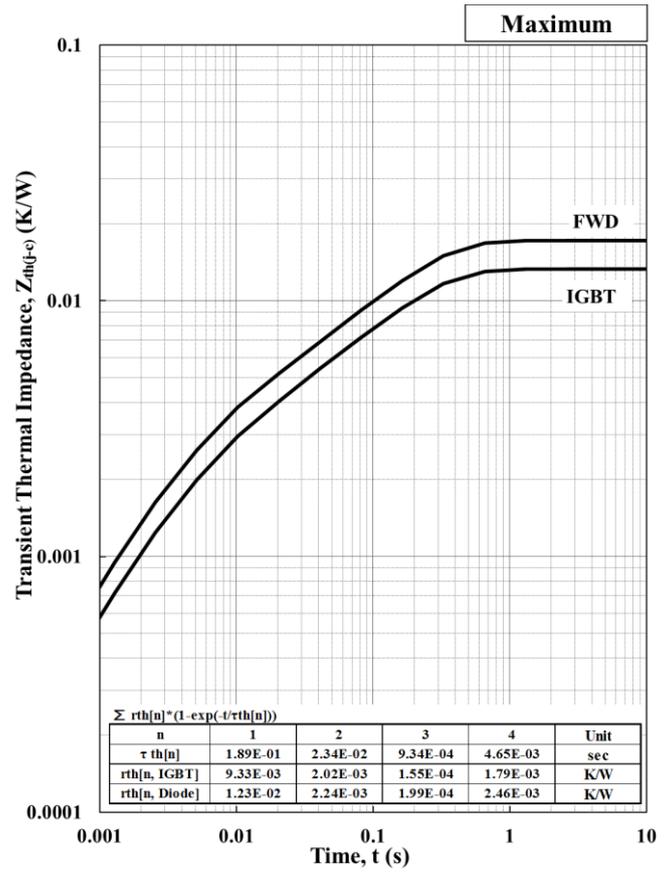
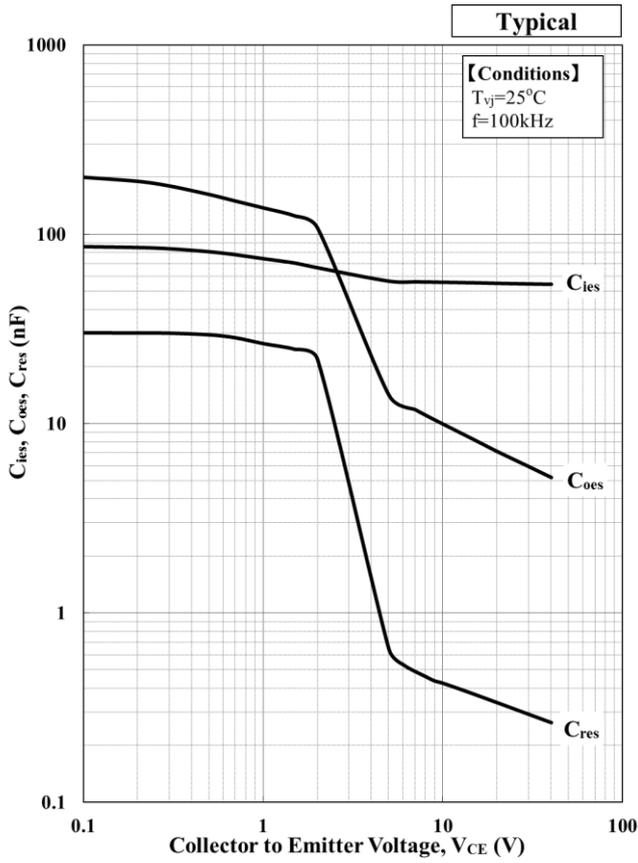


Reverse bias safe operation area (RBSOA)



Reverse recovery safe operation area (RRSOA)

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HITACHI POWER SEMICONDUCTORS

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6. This specification is a material for component selection, which describes specifications of power semiconductor devices (hereinafter referred to as products), characteristic charts, and external dimension drawings.
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